

**Arduino to STM32 Pin Allocation Table (rev 1)**

Adruino Pin #	F103RB Nucleo	Generic F103RB	Generic F103R8	Generic F103C8/CB	Adruino Pin #
0	PA3 (see note 3)	PA0	PA0	PA0	0
1	PA2 (see note 3)	PA1	PA1	PA1	1
2	PA10	PA2 (see note 3)	PA2 (see note 3)	PA2 (see note 3)	2
3	PB3	PA3 (see note 3)	PA3 (see note 3)	PA3 (see note 3)	3
4	PB5	PA4	PA4	PA4	4
5	PB4	PA5	PA5	PA5	5
6	PB10	PA6	PA6	PA6	6
7	PA8	PA7	PA7	PA7	7
8	PA9	PA8	PA8	PA8	8
9	PC7	PA9	PA9	PA9	9
10	PB6	PA10	PA10	PA10	10
11	PA7	PA11	PA11	PA11	11
12	PA6	PA12	PA12	PA12	12
13	PA5	PA13	PA13	PA13 (see note 2)	13
14	PB9	PA14	PA14	PA14 (see note 2)	14
15	PB8	PA15	PA15	PA15 (see note 2)	15
16	PA0	PB0	PB0	PB0	16
17	PA1	PB1	PB1	PB1	17
18	PA4	PB2 (see note 1)	PB2 (see note 1)	PB2 (see note 1)	18
19	PB0	PB3	PB3	PB3 (see note 2)	19
20	PC1	PB4	PB4	PB4 (see note 2)	20
21	PC0	PB5	PB5	PB5	21
22	PC10	PB6	PB6	PB6	22
23	PC12	PB7	PB7	PB7	23
24	PB7	PB8	PB8	PB8	24
25	PC13	PB9	PB9	PB9	25
26	PC14	PB10	PB10	PB10	26
27	PC15	PB11	PB11	PB11	27
28	PC2	PB12	PB12	PB12	28
29	PC3	PB13	PB13	PB13	29
30	PC11	PB14	PB14	PB14	30
31	PD2	PB15	PB15	PB15	31
32	PC9	PC0	PC0	PC13	32
33	PC8	PC1	PC1	PC14	33
34	PC6	PC2	PC2	PC15	34
35	PC5	PC3	PC3		35
36	PA12	PC4	PC4		36
37	PA11	PC5	PC5		37
38	PB12	PC6	PC6		38
39	PB11	PC7	PC7		39
40	PB2	PC8	PC8		40
41	PB1	PC9	PC9		41
42	PB15	PC10	PC10		42
43	PB14	PC11	PC11		43
44	PB13	PC12	PC12		44
45		PC13	PC13		45
46		PC14	PC14		46
47		PC15	PC15		47
48		PD0	PD2		48
49		PD1			49
50		PD2			50

**Note 1** For Generic F103R the Arduino Pin 18 cannot be used. It is actually PB2, but PB2 is used as BOOT1 which is usually tide LOW as part of the programming/running boot configuration.

**Note 2** These pins are busy when connecting a JTAG programming device. It is best to avoid using these pins.

**Note 3** These pins are multipurpose and become RX and TX when the B4R “Serial” class is initialised. It’s best not to use them.